Ante Brizić

Branimirova 16, 10 430 Samobor, Croatia

brizic.ante@gmail.com
+385 98 978 7903
LinkedIn

WORK EXPERIENCE

Eridan Communications, [Zagreb, Croatia],[Sunnyvale, California]

RF Systems Engineer

June 2021 – Present

- Digital predistortion / DPD and calibration of a state of the art GaN RF output stage based radio unit
- System power supply design based on a PMIC architecutre (45W consumption

    Developed, simulated and analyzed the whole power architecture

    Tested the potential devices in laboratory prior to board manufacturing

- RF measurements involving compliance with 3GPP specs for LTE/5G (ACLR, EVM).
- Laboratory measurements and debug/repair of in-house RF PBCs etc..

    Extensive usage of spectrum analyzers, signal generators, vector network analyzers, measurements of phase noise etc..

    Measurement automatisation

- Measurements on state of the art GaN integrated circuits
- System modeling, RF PCB design & simulations
- System/RU power supply design, analysis & measurements
- Small team lead (up to 3 persons)

Faculty of Electrical Engineering and Computing, Zagreb, Croatia

Research scientist - RFIC engineering

October 2019 – June 2021

- 40nm TSMC Cadence design basic(SPICE simulations, analysis, current mirrors etc.)
- Mathematical analysis and numerical verification (ADS, MATLAB) of electrical schematics
- Multi-switch FPGA project project
- Course coordinator and laboratory excerises helper for Radio-frequency and Microwave Measurements and Applied Electromagnetics courses
- RF measurements (antenna, rectangular waveguides, transmission lines), writing automatization programs for VNA, spectrum analyzers, power meters (core MATLAB)

Geolux d.o.o., Zagreb, Croatia

Application for testing and verification of radar devices - summer internship

July 2018 – Aug 2018

- Development of an application used for testing and verification of high frequency (24 GHz) radars
- Development was primarily done using C and C# programming languages
- Gained experience in working with toolchain used to develop applications for embedded devices based on ARM processors
- Extensive usage of high frequency test equipment (eg. spectrum analysers)

EDUCATION

University of Zagreb, Faculty of Electrical Engineering and Computing, Croatia

Graduate study - Electronic and Computer Engineering - Credit courses   2017 – 2019

- MSc Thesis: *Switch-mode radiofrequency amplifiers with complementary filters*
- Analog and Mixed Signal Processing
- Analog and Mixed-signal Integrated Circuits
- Design of Radio-frequency and Microwave Integrated Circuits
- Radio-Frequency Electronics
- Radio-Frequency and Microwave Measurements
- Innovative Electromagnetic Systems - from Idea to Practical Realization
- Signal Processing in Communications

TECHNICAL SKILLS

Intermediate	• **Altium Designer**
	• **Matlab**
	• **C**
	• **LTSpice**
	• **LaTeX**
Basic	• **Cadence Virtuoso, Spectre**
	• **ADS**
	• **KiCad**
	• **Python**
	• **Embedded C**
	• **C#**
	• **Linux**

OTHER SKILLS AND INTERESTS

Languages	Croatian (native), English (fluent), German (basisch), French (débutant)
Interests	RF design, IC design, Hardware design, Analog and Mixed Signal Processing
Hobbies	Climbing, Hiking, guitar